

**UTKAL GOURAV MADHUSUDAN INSTITUTE OF TECHNOLOGY (U.G.M.I.T.), RAYAGADA**

**TIME TABLE FOR 3RD & 5TH SEMESTER (E.T.C.) FOR THE SESSION 2024-25 (WINTER)**

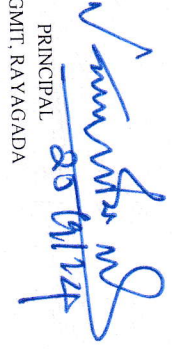
DAY	SEM.	9.30-10.25AM	10.25-11.20AM	11.20-12.15PM		LUNCH BREAK				2.15-3.10PM		3.10-4.05PM	4.05-5.00PM
				12.15PM	12.15-1.10PM	SCA	PROJECT PHASE-1(AS & SS)	DE LAB(SKB & JPG)	SCA	DE LAB(SKB & JPG)	SCA		
MON	3rd	CT(AS)		MATH-III(PKT)						PROJECT PHASE-1(AS & SS)		SCA	
	5th	A&DC (SNP)		EMS(SKB)						C PROGRAMMING & WEB PAGE DESIGN LAB(SKK & SSS)			
TUES	3rd	CT & SIM LAB(AS & SS)		DE(AS)						ADC LAB (SNP & SS)			
	5th	PE/GF ELECTRICAL)	A&DC(SNP)	WP&BC(SNP)		EMS(SNP)		VLSI LAB(AS & SS)		DE LAB(SKB & JPG)			
WED	3rd	MATH-III(PKT)		DE(AS)		EMS(SNP)		VLSI LAB(AS & SS)		DE LAB(SKB & JPG)			
	5th	SCA		VLSI(AS)		SCA							
THUS	3rd	MATH-III(PKT)		EVS(SKB)		EMI LAB(SNP & JPG)		PE LAB (GF ELECTRICAL)		C PROGRAMMING & WEB PAGE DESIGN LAB(SKK & SSS)			
	5th	PE/GF ELECTRICAL)		EVS(SKB)		EMI LAB(SNP & JPG)		PE LAB (GF ELECTRICAL)		C PROGRAMMING & WEB PAGE DESIGN LAB(SKK & SSS)			
FRI	3rd	EVS(SKB)		EMI LAB(SNP & JPG)		DE(AS)		PE LAB (GF ELECTRICAL)		C PROGRAMMING & WEB PAGE DESIGN LAB(SKK & SSS)			
	5th	VLSI(AS)		A&DC (SKB)		PROJECT-1(AS & SS)		WP&BC LAB(SNP & SS)		WP&BC LAB(SNP & SS)			
SAT	3rd	CT(AS)		CT & SIM LAB(AS & SS)		SCA							
	5th	A&DC (SKB)	PE/GF ELECTRICAL)	WP&BC(SNP)		SCA							

**NAME OF FACULTIES:-**

- 1.SNP-SATYA NARAYAN PANIGRAHI SKB-SUBHEN KUMAR BEHERA
- 2.AS-ANURAG SETHY GF ELECTRICAL-KISHORE CHANDRA PRUSTY
- 3.SKK-SISIR KUMAR KAPAT
- 4.PKT-PRADEEP KUMAR TRIPATHY
- 4.SS-SUSILA SAHOO(LAB ASSISTANT ETC)
- 5.JP GIRI-JYOTI PRAKASH GIRI (LAB ASSISTANT ETC)
- 6.SSS-SIBA SANKAR SAHU(LAB ASSISTANT COMPUTER APPLICATION)

HOD & TC 

ACADEMIC COORDINATOR 

PRINCIPAL   
UGMIT, RAYAGADA